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Docket No. 501.42822X00

Serial No. 10/618,748

Office Action dated January 11, 2006**AMENDMENTS TO THE CLAIMS**

The following listing of claims replaces all prior listings, and all prior versions, of claims in the application.

**LISTING OF CLAIMS:**

1. (Previously Presented) A liquid crystal display device comprising a first substrate, a second substrate, a liquid crystal layer between the first substrate and the second substrate, wherein

the first substrate has a pixel area, a peripheral area, gate lines, drain lines, first gate connecting lines, second gate connecting lines, a first insulating film, and a second insulating film,

the pixel area includes pixel electrodes, the gate lines and the drain lines,

the peripheral area surrounds the pixel area,

the gate lines include first gate lines and second gate lines,

first gate connecting lines and second gate connecting lines are disposed in the peripheral area,

the respective first gate connecting lines electrically connect the first gate lines to a liquid crystal driving circuit,

the respective second gate connecting lines electrically connect the second gate lines to the liquid crystal driving circuit,

the first insulating film insulates the first gate connecting lines from the second gate connecting lines,

the first gate connecting lines and the second gate connecting lines are stacked in a thickness direction of the first substrate,

the second insulating film is formed as a higher layer than the first gate connecting lines within the first substrate, and

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the second insulating film is formed in the pixel area and the peripheral area.

2. (Previously Presented) A liquid crystal display device according to claim 1, wherein the first gate lines are more distant from the liquid crystal driving circuit than are the second gate lines, and the first gate connecting lines are positioned at a higher level than are the second gate connecting lines within the first substrate.

3. (Canceled).

4. (Withdrawn) A liquid crystal display device comprising:  
a first substrate and a second substrate disposed in opposition to each other;  
and

a liquid crystal layer interposed between the first substrate and the second substrate,

the first substrate including gate lines extending in a lateral direction, drain lines extending in a longitudinal direction, pixel electrodes, and charge-holding capacitance lines extending in parallel with the gate lines,

capacitors being formed between the pixel electrodes and common electrodes disposed in opposition to the pixel electrodes,

the common electrodes being electrically connected to the charge-holding lines via a common line,

the gate lines being disposed under the common line in an insulated state.

5. (Withdrawn) A liquid crystal display device according to claim 4, wherein the common line extends in parallel with the drain lines.

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6. (Withdrawn) A liquid crystal display device comprising a liquid crystal layer between a first substrate and a second substrate,

the first substrate including gate lines and drain lines in a pixel area, a gate driver and a drain driver being provided in a peripheral area surrounding the pixel area, the gate lines being electrically connected to the gate driver by gate connecting lines, the drain lines being electrically connected to the drain driver by drain connecting lines,

the drain connecting lines passing through a portion under the gate driver and electrically connecting the drain lines to the drain driver.

7. (Withdrawn) A liquid crystal display device according to claim 6, wherein the drain driver has a rectangular shape with short sides and long sides, the drain connecting lines being electrically connected to a short side of the drain driver.

8. (Previously Presented) A liquid crystal display device according to claim 1, wherein one of the first gate connecting lines and one of the second gate connecting lines overlap one another when viewed in plan view.

9. (Previously Presented) A liquid crystal display device according to claim 1, wherein one of the first gate connecting lines is disposed between the two second gate connecting lines which are adjacent one another when viewed in plan view.

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10. (Currently Amended) A liquid crystal display device comprising a first substrate, a second substrate, a liquid crystal layer between the first substrate and the second substrate, wherein

the first substrate has a pixel area, a peripheral area, gate lines, drain lines, first gate connecting lines, and second gate connecting lines,

the pixel area includes pixel electrodes, gate lines, and drain lines,

the peripheral area surrounds the pixel area,

the gate lines include first gate lines and second gate lines,

the first gate connecting lines and the second gate connecting lines are disposed in the peripheral area,

~~the respective first gate connecting lines and the second gate connecting lines are disposed in the peripheral area,~~

the respective first gate connecting lines electrically connect the first gate lines to a liquid crystal driving circuit,

the respective second gate connecting lines electrically connect the second gate lines to the liquid crystal driving circuit,

the first gate connecting lines and the second gate connecting lines are stacked in a thickness direction of the first substrate, and

one of the first gate connecting lines is disposed between two second gate connecting lines which are adjacent one another when viewed in plan view.

11. (Currently Amended) A liquid crystal display device comprising a first substrate, a second substrate, a third substrate, a fourth substrate, a first liquid crystal layer between the first substrate and the second substrate, and a second liquid crystal layer between the third substrate and the fourth substrate, wherein

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the first substrate has a first pixel area, a first peripheral area, first gate connecting lines, and second gate connecting lines,

the third substrate has a second pixel area and a second peripheral area,

the first pixel area has first pixel electrodes, first gate lines, and first drain lines,

the first peripheral area surrounds the first pixel area,

the second pixel area has second pixel electrodes, second gate lines, and second drain lines,

the second peripheral area surrounds the second pixel area,

the first gate connecting lines and the second gate connecting lines are disposed in the first peripheral area,

the respective first gate connecting lines electrically connect the first gate lines to a liquid crystal driving circuit,

the respective second gate connecting lines electrically connect the second gate lines to the liquid crystal driving circuit, and

the first gate connecting lines and the second gate connecting lines are stacked in a thickness direction of the first substrate.

12. (Previously Presented) A liquid crystal display device according to claim 11, wherein

the first substrate has a first insulating film and a second insulating film,  
the first insulating film insulates the first gate connecting lines from the second gate connecting lines,

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the second insulating film is formed as a higher layer than the first gate connecting lines and the second gate connecting lines with respect to the first substrate, and

the second insulating film is formed in the first pixel area and the first peripheral area.

13. (New) A liquid crystal display device according to claim 10, wherein the respective second gate connecting lines electrically connect the second gate lines, which are disposed in the third substrate, to the liquid crystal driving circuit.

14. (New) A liquid crystal display device according to claim 11, wherein the respective second gate connecting lines electrically connect the second gate lines, which are disposed in the third substrate, to the liquid crystal driving circuit.